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## UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	:	Group Art Unit:
Victor R. Augsburg et al.	:	Not Yet Assigned
	:	
Serial No.:	:	Not Yet Assigned
	:	
Filed:	:	IBM Corporation
(Herewith)	:	Intellectual Property Law
	:	3039 Cornwallis Rd.
Title:	:	Research Triangle Park, NC 27709
EFFICIENTLY CALCULATING	:	
A BRANCH TARGET ADDRESS	:	

#2  
C. Bures  
5/14/02

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

This Information Disclosure Statement is being submitted in connection with the above-identified application for patent. Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 C.F.R. § 1.98(a)(1).

A copy of each of the items identified on the attached Form PTO-1449 is supplied herewith, except for the pending patent applications, for which no copies are being submitted.

Respectfully submitted,

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**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANTS' INFORMATION DISCLOSURE  
STATEMENT**

jc903 U.S. PTO  
10/082144  
02/25/02

Reference Designation

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
____AAA	5,737,561	04/07/98	Dulong	395	391	
____ABC	5,878,254	03/02/99	Shimada et al.	395	585	
____ACA	5,790,845	08/04/98	Shimada et al.	395	585	
____ADA	5,367,649	11/22/94	Cedar	395	375	
____AEA	5,522,053	05/28/96	Yoshida et al.	395	421.03	
____AFA	5,778,423	07/07/98	Sites et al.	711	118	
____AGA	5,796,998	08/18/98	Leviton et al.	395	586	
____AHA	5,142,634	08/25/92	Fite et al.	395	375	
____AIA						
____AJA						
____AKA						
____ALA						
____AMA						
____ANA						

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
____AOA	WO99/19793	04/22/99	PCT			X
____APA	GB2250840A	11/23/93	UK			X
____AQA						

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**Examiner  
Initial

- ✓ \_\_\_\_ARA B. Appelbe et al. "Hoisting Branch Conditions – Improving Super-Scalar Processor Performance," *Languages and Compilers for Parallel Computing, 8<sup>th</sup> International Workshop, LCPC '95*, August 1995, pp. 304-317.
- ✓ \_\_\_\_ASA J. C. Chan et al. "Compiler-Driven Hybrid Dynamic Branch Predictor," *IBM Technical Disclosure Bulletin*, Vol. 36, No. 02, February 1993, pp. 127-130.
- ✓ \_\_\_\_ATA A. G. Liles et al. "Branch Prediction Mechanism," *IBM Technical Disclosure Bulletin*, Vol. 22, No. 7, December 1979, pp. 3013-3016.
- \_\_\_\_AUA V. R. Augsburg et al., Pending Patent Application, docket No. RPS920010178US1 "RE-ENCODING ILLEGAL OP CODES INTO A SINGLE ILLEGAL OP CODE TO ACCOMMODATE THE EXTRA BITS ASSOCIATED WITH PRE-DECODED INSTRUCTIONS"

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.